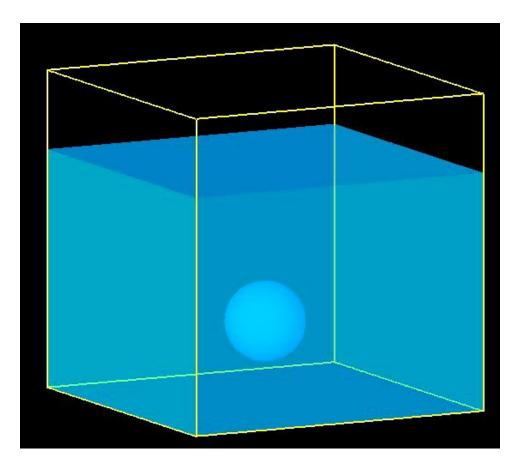
Implementation of Deflated Preconditioned Conjugate Gradient on the GPU

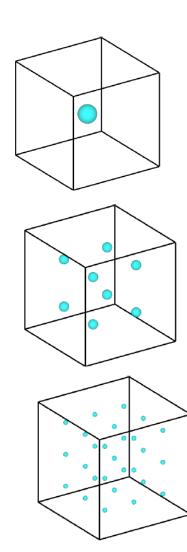
Rohit Gupta

Responsible Professor: Prof. Dr. Ir. Henk Sips Supervisors: Prof. Dr. Ir. Kees Vuik and Ir. C.W.J. Lemmens

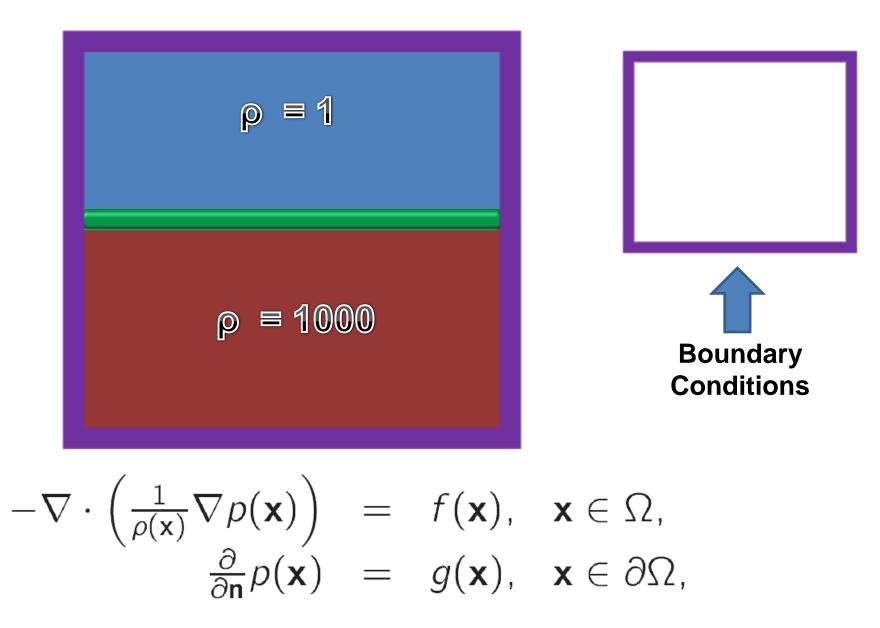


Two-Phase Fluid Flow



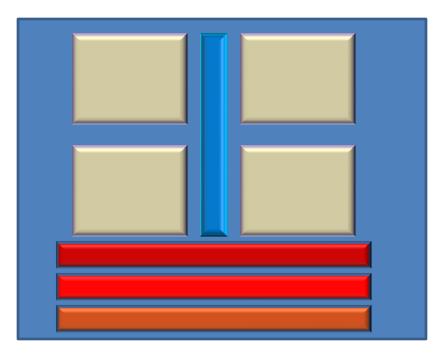


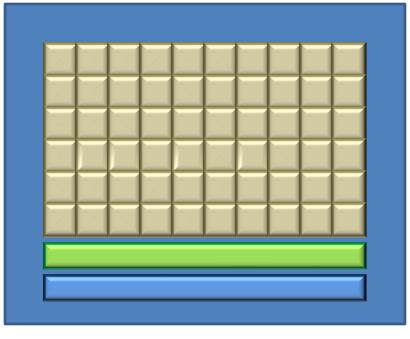
Model for Two Phase Computation



3	đ						
ሳ	1.5015	-0.5 0 05					
	-0.5 0 05	2.002	-0.5 0 05				
		-0.5 0 05	2.002	-0.5005			
			0 -0.5005	4 2.002	-1 -0.5005		
				-1 -0.5005	4 2.002	-1 -0.5005	
					-1 -0.5005	4 2.002	-1 -0.5005
						-1 -0.5005	4 2.002

GPU Architecture



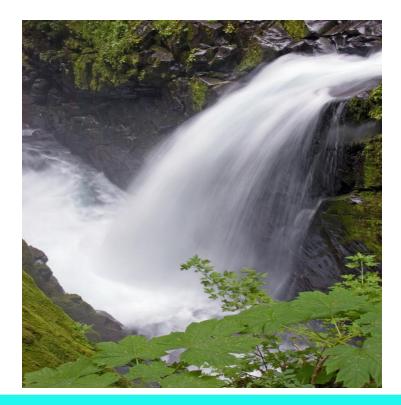


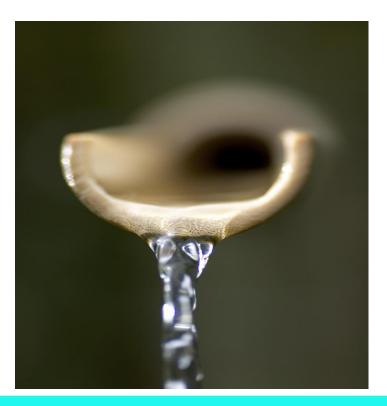






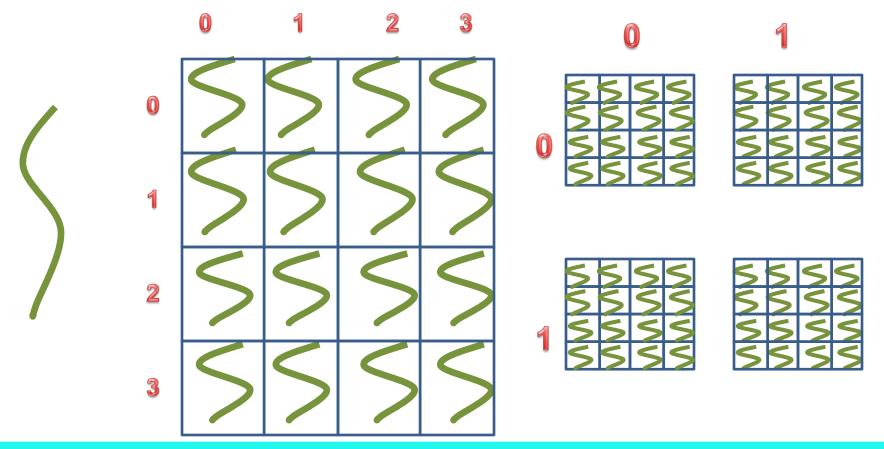
Memory Bandwidth







Launch Configuration





Key Optimizations

- Coalescing
- Minimum Memory Transfers
- Minimum Divergence
- Caching



Related Work

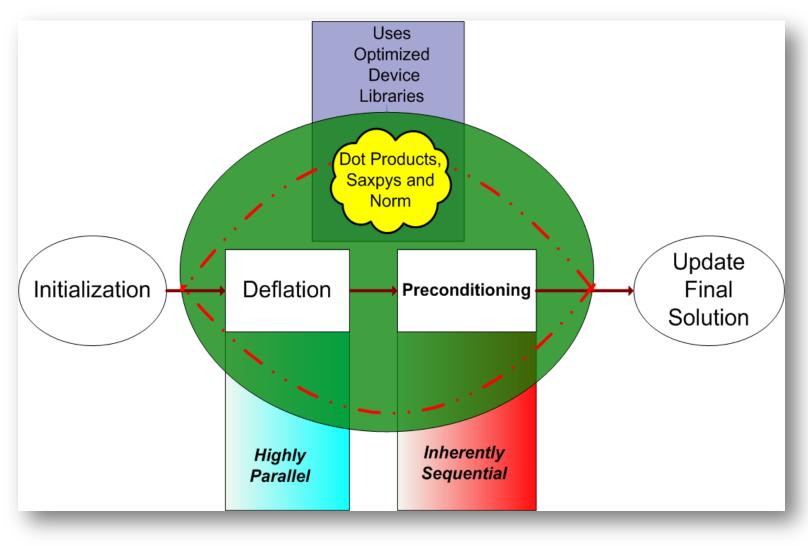
SpMV Kernel

Preconditioning Kernels

Deflation Kernels



Two-Phase Fluid Flow



Implementation GPU

SpMV Kernel

Preconditioning Kernels

Deflation Kernels



Implementation CPU

Meschach/ gotoBLAS

Compiler Options

Same Data Structures





- Conjugate Gradient (CG)
- CG with Preconditioning
- •CG with Preconditioning and Deflation

Delft

Preconditioning

Diagonal

Incomplete Cholesky

Incomplete Poisson





Important Operations

Subdomains

Deflation Kernels



Methodology

Step-by-Step Optimizations

Profiler for Advice

Modularity and Readability



Conjugate Gradient - Vanilla

DIA Format is best

SpmV dominate GPU execution

•
$$10^{-3} < \frac{\|X_{exact} - X_k\|_2}{\|X_k\|_2} < 10^{-5}$$



Preconditioning Block Incomplete Cholesky

Preconditioning dominates execution

More Blocks – Better SpeedUp

Shared Memory Use restricted



Deflation

- Speed Up recovers
- •Preconditioning gets company in $AZ \times E^{-1}b$
- More Deflation Vectors More Speed Up



Deflated Preconditioned Conjugate Gradient

•Storage of AZ optimized

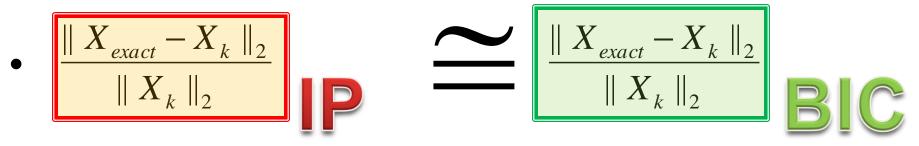
•Speed Up Suffers – CPU performs better

Optimizations on CPU



Deflated Preconditioned Conjugate Gradient

- Incomplete Poisson Preconditioning
- •Speed Up favoring move





Deflated Preconditioned Conjugate Gradient

•Matrix Vector Multiplication $E^{-1}b$ optimized

- •Kernels utilize 85% of bandwidth
- Clubbing Kernels Reducing Memory Traffic



Two Phase Matrix

Density Contrast 1000:1

• 0 <
$$\frac{\|X_{exact} - X_k\|_2}{\|X_k\|_2} < 10^{-1}$$

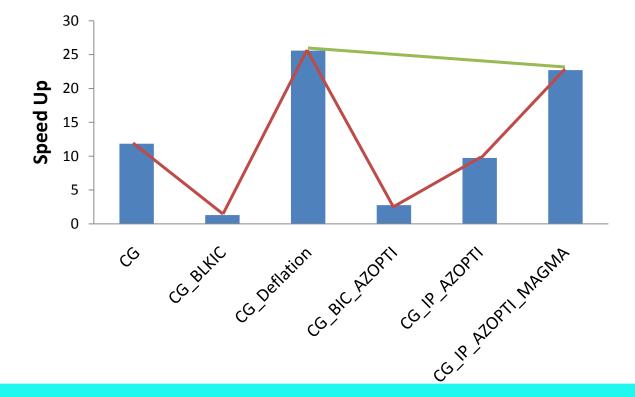
•False Stop



Performance Timeline

Iterative Optimizations

SpeedUp Factors





Analysis

- Very close to possible peak performance
- Bandwidth bound Kernels
- Platform Utilization Startling Facts



Future Work

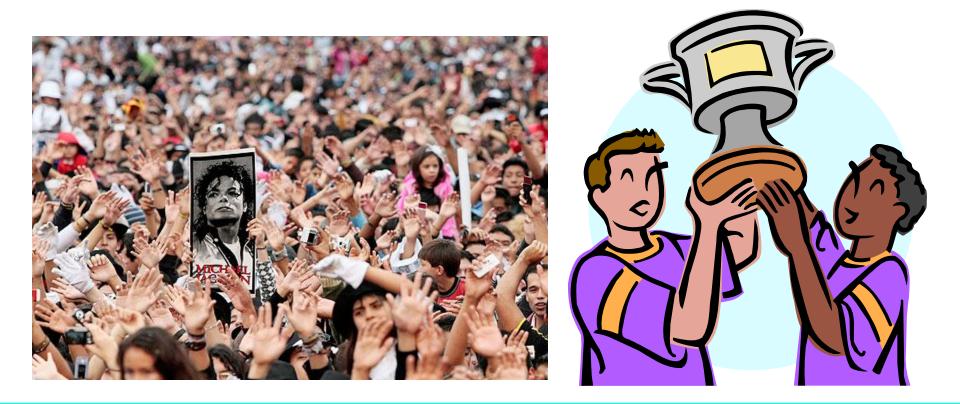
•Multi-GPU Multi-CPU

•3D Domains, More Interfaces, Mixed Precision

•Different Preconditioners, Grid Types.



Conclusions





Conclusions

- Deflation suits the many core platform
- •Two Phase accuracy suffers
- Deflation with IP Preconditioning

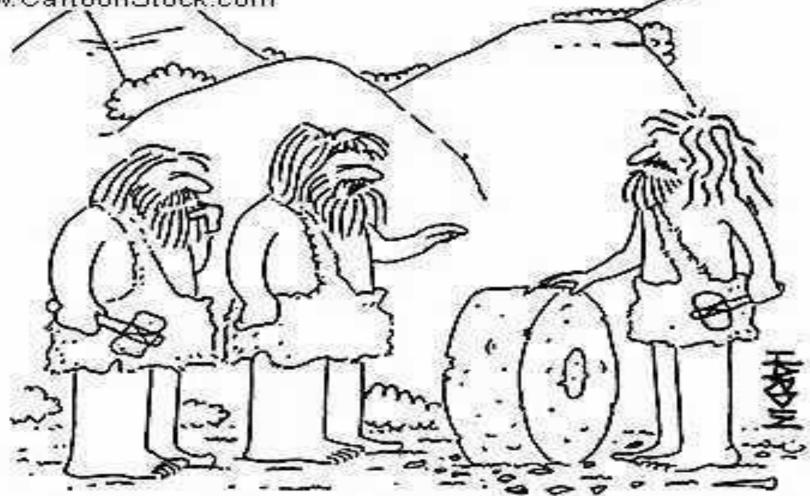




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"IMPLEMENTING THESE CHANGES WON'T BE EASY. WE'RE PRETTY SET IN DOING THINGS THE WRONG WAY." © Original Artist Reproduction rights obtainable from www.CartoonStock.com



pha0158

"This 'Wheel' thing of yours-Does it have to be round or will any shape do?"